AMENDMENTS TO THE SPECIFICATION

Please replace the paragraph beginning on page 2, line 5 with the following amended paragraph:

Fig. 2 shows an example of a timing diagram of signals RD, RST, T_{X_2} of voltage V_S between read node S and the circuit ground, and of voltage V_D across photodiode D of the circuit of Fig. 1 between two read cycles of the photosensitive cell. Signals RD, RST, and T_X are binary signals varying between high and low levels that can be different for each of the signals.